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PATENT

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Type or print name of person signing

Applicant Application No.: 09/383,150

: Rong-Fuh Shyu

Filed Title

: August 25, 1999

: LEAD FRAME FOR A

SEMICONDUCTOR CHIP PACKAGE, SEMI CONDUCTOR CHIP PACKAGE

INCORPORATING MULTIPLE INTEGRATED CIRCUIT CHIPS,

AND METHOD OF FABRICATING... **FAX COPY RECEIVED** 

FEB 2 1 2001

**TECHNOLOGY CENTER 2800** 

Grp./Div.

: 2826

Examiner

: E. Abraham

: 35761/DBP/S295 Docket No.

#### REQUEST FOR RE-SETTING OF SHORTENED TIME FOR RESPONSE AND FOR PROPER CITATION OF CITED RAAD REFERENCE

Assistant Commissioner for Patents Washington, D.C. 20231

Post Office Box 7068 Pasadena, CA 91109-7068 February 20, 2001

The February 9, 2001 official Office Action appears to contain two deficiencies as follows:

- This is a first official Office Action with no claims 1. allowed, and the claims are rejected based on the art. However, the Examiner set a one-month period for response expiring March 9, 2001. This is clearly improper, and it is requested that a proper three-month period for response be afforded.
- The official Office Action cites the Raad reference. 2. However, the number of the Raad reference is not

### Application No. 09/383,150

contained in an IDS cited by the applicant nor in the U.S. Patent and Trademark Office Form 892 provided by the Examiner, nor has a copy been provided.

Accordingly, it is requested that a new Office Action be issued properly identifying and providing a copy of the Raad reference and resetting the date for response running three months from the date of the new official action.

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

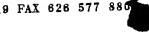
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D. Bruce Proof. Reg. No. 20,958 626/795-9900

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D. Bruce Prout

Reg. No. 20,958

Re:

Patent Application No. 09/383,150; Filed August 25, 1999

Entitled LEAD FRAME FOR A SEMICONDUCTOR CHIP PACKAGE, SEMI

CONDUCTOR CHIP PACKAGE INCORPORATING MULTIPLE

INTEGRATED CIRCUIT CHIPS,...

File:

35761/DBP/S295

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